

ABSTRACT OF THE DISCLOSURE

A circuit for removing noise from the power supply of a clock generator. Switching regulator noise from the voltage regulator section can affect a clock chip when it is carried through the power plane or ground plane. In order to remove this, a ferrite bead is placed along the voltage supply line. A bulk capacitor with low equivalent series resistance is connected from the output side of the ferrite bead to ground. By properly selecting the size of the bead and capacitor, the noise is reduced. The result is that the clock does not drift so that functional failures are reduced.